

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515002 A.P. (INDIA)

Examinations Branch

Notification

for

M.Tech (Seer Akademi) I semester Regular & Supplementary March 2016 Examinations (For 2013, 2014 & 2015 admitted batches only)

The students appearing for the above examinations commencing from <u>14.03.2016</u> are informed that the applications will be received as per the time schedule given below:

Without Late Fee	29.02.2016
With Late Fee of Rs. 100/-	02.03.2016
With Late Fee of Rs. 1000/-	04.03.2016
With Late Fee of Rs.3000/-	08.03.2016
With Late Fee of Rs.5000/-	10.03.2016

		EXAMINATION FEE	-
1.	FORF	REGULAR (WHOLE EXAMINATION)	Rs.900-00
2.	EACH	THEORY/PRACTICAL SUBJECT (FOR SUPPLEMENTARY)	Rs.600-00
3.	MEMO	DRANDUM OF MARKS	Rs.10-00
4.	COST	OF APPLICATION	Rs.05-00

- Note: (i) Applications are available at the office of the Co-ordinator, Seer Akademi M.Tech programs, JNTUA, Ananthapuramu and duly filled-in applications should be handed over in the college office along with necessary fee.
 - (ii) Hall tickets will be issued only to the eligible candidates who fulfill the academic requirements of the University.
 - (iii) Applications for regular examinations should be submitted along with attested photocopies of SSC of the student.
 - (iv) Applications for supplementary examinations should be submitted along with attested photocopies of marks sheets pertaining to earlier attempts made by the student.



Date: 19.02.2016

Note:

- 1. For applications without late fine, the demand drafts/challans are to be dated 01.03.2016.
- 2. For applications with late fine of Rs.100.00, the demand drafts/challans are to be dated 03.03.2016.
- 3. For late fee of Rs.1000.00, the demand drafts/challans are to be dated 05.03.2016.
- 4. For late fee of Rs.3000.00, the demand drafts/challans are to be dated 09.03.2016.
- 5. For late fee of Rs.5000.00, the demand drafts/ challans are to be dated 11.03.2016.

Demand Drafts/Challans along filled in applications have to be submitted at the University Examinations Branch

- (i) on 08.03.2016 (Tuesday) for DDs without fine, with Rs.100/- fine and Rs.1000/- fine only.
- (ii) on 11.03.2016 (Friday) for DDs with Rs.3000/- and Rs.5000/- fine only.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ANANTHAPURAMU - 515002 A.P. (INDIA)



Notification for

M.Tech (Seer Akademi) II semester Supplementary March 2016 Examinations (For 2013 & 2014 admitted batches only)

The students appearing for the above examinations commencing from <u>14.03.2016</u> are informed that the applications will be received as per the time schedule given below:

Without Late Fee	29.02.2016
With Late Fee of Rs. 100/-	02.03.2016
With Late Fee of Rs.1000/-	04.03.2016
With Late Fee of Rs.3000/-	08.03.2016
With Late Fee of Rs.5000/-	10.03.2016

	EXAMINATION FEE	
1.	EACH THEORY/PRACTICAL SUBJECT	Rs.600-00
2. MEMORANDUM OF MARKS Rs.10-		Rs.10-00
3.	COST OF APPLICATION	Rs.05-00

- Note: (i) Applications are available at the office of the Co-ordinator, Seer Akademi M.Tech programs, JNTUA, Ananthapuramu and duly filled-in applications should be handed over in the college office along with necessary fee.
 - (ii) Hall tickets will be issued only to the eligible candidates who fulfill the academic requirements of the University.
 - (iii) Applications for supplementary examinations should be submitted along with attested photocopies of marks sheets pertaining to earlier attempts made by the student.

DR OF EVALUATION

Date: 19.02.2016

Note:

- 1. For applications without late fine, the demand drafts/challans are to be dated 01.03.2016.
- 2. For applications with late fine of Rs.100.00, the demand drafts/challans are to be dated 03.03.2016.
- 3. For late fee of Rs.1000.00, the demand drafts/challans are to be dated 05.03.2016.
- 4. For late fee of Rs.3000.00, the demand drafts/challans are to be dated 09.03.2016.
- 5. For late fee of Rs.5000.00, the demand drafts/ challans are to be dated 11.03.2016.

Demand Drafts/Challans along filled in applications have to be submitted at the University Examinations Branch

(i) on 08.03.2016 (Tuesday) for DDs without fine, with Rs.100/- fine and Rs.1000/- fine only.

(ii) on 11.03.2016 (Friday) for DDs with Rs.3000/- and Rs.5000/- fine only.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR ADMINISTRATIVE BUILDING, ANANTHAPURAMU 515002 EXAMINATIONS BRANCH

Prof. K Rama Naidu Director of Evaluation

Lr. No. DE / JNTUA/ ATP/ M.Tech (Seer Akademi) I & II semester Reg & Supple Examinations / March 2016 dt. 19.02.2016

To

The Coordinator, M.Tech Seer Akademi programs, JNTU Anantapur, Ananthapuramu - 515 002 (AP)

Sir

You are requested,

- To send hard & soft copy (CD) of internal marks of M.Tech I semester Regular students (AY 2015-16) to <u>Dr. R.Bhavani, Associate Professor of Civil Engineering and Additional Controller of</u> <u>Examinations, Examinations Branch, New Examinations Building, JNTUAnantapur, Ananthapuramu –</u> <u>515002 (A.P)</u> on or before <u>11.03.2016 (Friday)</u>. Please superscribe the cover containing the hard copy of internal marks as M.Tech(Seer Akademi)/I sem/IM/March 2016 Exams/<0A>.
- To conduct laboratory examinations from <u>08.03.2016 to 12.03.2016</u> and send the award lists in sealed covers to the address mentioned above on or before <u>17.03.2016 (Thursday)</u> without fail. The cover may be superscribed as M.Tech(Seer Akademi)/I&IIsem/Reg&Supple/Mar 16 Exams/Lab Award Lists/<0A>

To submit the following at the time of submission of registration forms along with DDs/Challans (a) Detained List.

- (b) Condoned List along with DD/Challan towards condonation fee.
- (c) College Academic Committee (CAC) resolution approving detained and condonation Lists.
- (d) Copy of Clearance Certificate.
- (e) Copy of UCS Fee Receipt (AY 2015-16) (or proof of fee payment).

Yours faithfully, Director of Evaluation

Copy to The Controller of Examinations. The Additional Controller of Examinations. File.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTHAPURAMU - 515 002 (A.P.) - INDIA.

Examinations Branch

M.Tech (Seer Akademi) I Semester Regular & Supplementary March 2016 Examinations

Timetable

Time: 10:00 AM to 01:00 PM

Date / Day	For 2013, 2014 & 2015 admitted batches only	For 2013 admitted batches only
outo / ouy	VLSI System Design (VLSISD)	Embedded Systems (ES)
14.03.2016 Monday	Advanced Computer Architecture 13J01101/13J02101 (Common to VLSISD & ES)	
16.03.2016 Wednesday	Linux Programming and Scripting 13J01103/13J02104 (Common to VLSISD & ES)	
18.03.2016 Friday	Advanced Digital Logic Design 13J01102	Embedded Software Testing 13J02102
21.03.2016 Monday	Advanced Logic Synthesis 13J01104	Embedded Systems Design - 1 13J02103
24.03.2016 Thursday	Design for Testability for SoC 13J01105a	Embedded Systems Design - 2 13J02105a

Note: (i) Any omissions or clashes in this time table may please be informed to the undersigned immediately. (ii) If any discrepancies are found, the same may be informed to the undersigned immediately.

(iii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.

Controller of Examinations

Date: 19.02.2016



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR

ANANTHAPURAMU - 515 002 (A.P.) - INDIA.

Examinations Branch

M.Tech (Seer Akademi) II Semester Supplementary March 2016 Examinations

Timetable

Time: 10:00 AM to 01:00 PM

Date / Day	ä	2013 & 2014 admitted batches only		2013 admitted batches only	
Dure / Duy		VLSI System Design (VLSISD)		Embedded Systems (ES)	
15.03.2016 Tuesday	Digital VLSI Physical Design 13J01201			Real Time Operating Systems 13J02201	
17.03.2016 Thursday	Advanced Verification using System Verilog 13J01202			Protocols & Interfaces 13J02204	
19.03.2016 Saturday	Arm based Development 13J01203/13J02203 (Common to VLSISD & ES)				
22.03.2016 Tuesday		Protocols & Interfaces 13J01204	Kernel Programming & Driver Development 13J02202		
	N	Low Power Design for SoC 13J01205a	N	Hardware Software Co Design 13J02205a	
26.03.2016 Saturday	Elective	Digital Standard Cell Library Design 13J01205b	Elective	Embedded Systems Hardware Design 13J02205b	
		Advanced Physical Design 13J01205c		DSP for Embedded Systems 13J02205c	

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Controller of Examinations

Date: 19.02.2016