

# UNIVERSITY OF KERALA

## Time Table (Main)

### **Second Semester and Fourth Semester B.Tech Degree** **(Part time – Restructured, 2010 admission onwards) Examination, May 2014**

#### 2008 Scheme

Day	Date	Time	Part time Code	Reg Code	Papers
Friday	09 -05 -2014	9.30 am to 12.30 pm	P 10.201 P 10.201	08.401 08.401	Engineering Mathematics III (CMER) Engineering Mathematics III- Probability and Random Processes (T)
Tuesday	13 -05 -2014	9.30 am to 12.30 pm	P 10.403 P 10.401 P 10.202  P 10.203 P10.204	08.402 08.402 08.402  08.404 08.405	} Humanities (CTR) }  Electrical Measurements –I (E) Thermal Engg. (M)
Thursday	15 -05 -2014	9.30 am to 12.30 pm	P 10.203 P 10.401 P 10.402 P 10.401	08.403 08.403 08.403 08.403	Structural Analysis-I (C) Engineering Electro-Magnetics (E) Signals & Systems (T) Computer Hardware Design (R)
Monday	19 -05 -2014	9.30 am to 12.30 pm	P 10.203 P 10.203 P 10.202 P 10.203	08.404 08.404 08.404 08.404	Fluid Mechanics – II (C) Manufacturing Processes (M) Electronics Circuits II (T) Object Oriented Techniques (R)
Wednesday	21 -05 -2014	9.30 am to 12.30 pm	P 10.404	08.406	Building Planning & Drawing (C)
		9.30 am to 1.30 pm	P 10.403	08.406	Machine Drawing (M)
		9.30 am to 12.30 pm	P 10.204 P 10.204 P 10.402	08.406 08.406 08.406	Power System Engineering – I (E) Analog Communication (T) Operating Systems (R)
Friday	23 -05 -2014	9.30 am to 12.30 pm	P 10.204 P 10.402  P 10.402 P 10.203 P 10.204	08.405 08.402  08.405 08.405 08.405	Surveying II (C) Computer Programming and Numerical Methods (M) Engineering Material Science (E) Analog Integrated Circuits (T) Data Structures and Algorithms (R)
Monday	26 -05 -2014	9.30 am to 12.30 pm	P 10.402 P 10.202	08.403 08.402	Metallurgy & Material Science (M) Digital Electronics & Logic Design (E)