

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
(Established by the State Legislature Act XII of 1956)  
("A" Grade, NAAC Accredited)

**Date-sheet for the M.Tech. Computer Science and Engineering I & III SEMESTER**  
examination commencing w.e.f. 16-12-2014.

**(THEORY EXAMINATION)**

**Time of Examination: 9.30 A.M. to 12.30 P.M. (MORNING SESSION)**

DATE	SUBJECT & PAPER
<b>18-Dec-2014</b>	<b><u>I Semester</u></b> MT-CSE-110: Advanced Data Structure Using C++ (Old 2009-10) MT-CSE-14-11: Advances in Algorithms (New 2014-15)
<b>22-Dec-2014</b>	MT-CSE-120: Visual Programming and Web Technology (Old 2009-10) MT-CSE-14-12: Advanced Web Technologies (New 2014-15)
<b>27-Dec-2014</b>	MT-CSE-130: Advances in Database (Old 2009-10) MT-CSE-14-13: Data Warehousing & Data Mining (New 2014-15)
<b>31-Dec-2014</b>	MT-CSE-140: Advanced Computer Architecture (Old 2009-10) MT-CSE-14-14: Advanced Computer Architecture (New 2014-15)
<b>16-Dec-2014</b>	<b><u>III Semester</u></b> MT-CSE-310: System Simulation (Old Syllabus) MT-CSE-310: Advanced Optimization and Simulation Techniques
<b>20-Dec-2014</b>	MT-CSE-320: Visual Programming (Old Syllabus) MT-CSE-320: High Performance Networks
<b>24-Dec-2014</b>	MT-CSE-311: Microprocessors and Interfaces (Old Syllabus) MT-CSE-321: Computer Graphics and Multimedia (Old Syllabus) MT-CSE-331: Computer Systems Reliability (Old Syllabus) MT-CSE-330: (i) Advanced Microprocessors MT-CSE-330: (ii) Embedded Systems MT-CSE-330: (iii) Mobile Computing
<b>30-Dec-2014</b>	MT-CSE-341: Computer Design (Old Syllabus) MT-CSE-351: Artificial Intelligence and Expert System (Old Syllabus) MT-CSE-361: Software Quality Models and Testing (Old Syllabus) MT-CSE-340: (i) Soft Computing MT-CSE-340: (ii) Genetic Algorithms MT-CSE-340: (iii) Neural Networks and Fuzzy Logic

**NOTES : 1** On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.

**2** Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.

**3** No extra answer-sheet will be provided.

KURUKSHETRA  
18-Nov-2014

CONTROLLER OF EXAMINATIONS

(BUILDING NOTICE PRINTED ATTACHED)

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Date-Sheet for the M.Tech. Microelectronics & VLSI Design I & III Semester examination  
 commencing w.e.f. 09-12-2014.

**(THEORY EXAMINATION)**

Time of Examination: 9.30 A.M. to 12.30 P.M. (MORNING SESSION)

Examination Centre : A-867, Kurukshetra-44, Department of Electronics

DATE	SUBJECT & PAPER
	<b><u>I Semester</u></b>
9-Dec-2014	Paper-MMVD 1.1: Process Technology for ULSI-I
13-Dec-2014	Paper-MMVD 1.2: Modeling Techniques for Submicron Devices
16-Dec-2014	Paper-MMVD 1.3: VLSI Design-system on Silicon
19-Dec-2014	Paper-MMVD 1.4: Hardware Description Languages
	<b><u>III Semester</u></b>
11-Dec-2014	MMVD 3.1 (ii) Digital System Testing and Fault Simulation
15-Dec-2014	MMVD 3.2 (i) MEMS and Nanotechnology

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