

OFFICE OF THE CONTROLLER OF EXAMINATIONS

MAHATMA GANDHI UNIVERSITY NALGONDA - 508 254

No.12 /Exams/MGU/2016

TIME-TABLE

B.Tech. (CSE /ECE/EEE) II-Year I-Semester Examinations will be held in December, 2016 as per the following schedule.

Time: 10.00 to 1.00 pm

| Date & Day | Paper | CSE | ECE | EEE |
|-------------------------|-------|-------------------------------|------------------------------------|---|
| 14.12.2016 Wednesday | ı | Mathematics-III | Mathematics-III | Mathematics-III |
| 16.12.2016 Friday | 11 | Data Structures Using C++ | Electronics Devices | Electrical Circuits-I |
| 19.12.2016 Monday | 111 | Discrete Structures | Electrical Technology | Environmental Studies |
| 21.12.2016 Wednesday | IV | Logic and Switching Theory | Basic Circuit Analysis | Electrical Measurements and Instruments |
| 23.12.2016 Friday | ν | Computer Architecture | Electromagnetic Theory | Electronic Engineering |
| 26.12.2016 Monday | VI | Basic Electronics | Elements of Mechanical Engineering | Principles of Mechanical Engineering |

Sd/-

Date: 05.12.2016

Controller of Examinations

Copy to:

- 1. The Principal UCET, MGU
- 2. Director, Academic Audit, MGU
- 3. P.S. to Vice-Chancellor, MGU
- 4. P.A. to Registrar
- 5. PRO, MGU



OFFICE OF THE EXAMINATION BRANCH

MAHATMA GANDHI UNIVERSITY NALGONDA - 508 254

No.13 /Exams/MGU/2016

TIME-TABLE

B.Tech. (CSE /ECE/EEE) III-Year I-Semester Examinations will be held in December, 2016 as per the following schedule.

Time: 2.00 to 5.00 pm

| Date & Day | Paper | CSE | ECE | EEE |
|------------------------|-------|--------------------------------------|--|---|
| 15.12.2016 Thursday | 1 | Database Management Systems | Linear Integrated Circuits and Applications | Power Systems-II |
| 17.12.2016 Saturday | 11 | Operating Systems | Digital Integrated Circuits and Applications | Electrical Machinery-II |
| 20.12.2016 Tuesday | 111 | Automata, Languages and Computation | Analog Communications | Power Electronics |
| 22.12.2016 Thursday | IV | Software Engineering | Automatic Control Systems | Digital Electronics and Logic Design |
| 24.12.2016 Saturday | ν | Managerial Economics and Accountancy | Computer organization and Architecture | Linear Integrated Circuits |
| 27.12.2016 Tuesday | VI | Design & Analysis of Algorithms | Electronic Instrumentation | Linear Control Systems |

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Date: 05.12.2016

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OFFICE OF THE EXAMINATION BRANCH

MAHATMA GANDHI UNIVERSITY NALGONDA - 508 254

Date: 05.12.2016

No.14 /Exams/MGU/2016

TIME-TABLE

B.Tech. (CSE /ECE/EEE) IV-Year I-Semester Examinations will be held in December, 2016 as per the following schedule.

Time: 2.00 to 5.00 pm

| Date & Day | Paper | CSE | ECE | EEE |
|-------------------------|-------|---|--|---------------------------------------|
| 14.12.2016 Wednesday | 1 | Distributed Systems | Microwave Engineering | Power System Operation and Control |
| 16.12.2016 Friday | 11 | Artificial Intelligence | VLSI Design | Electric Drives and Static Control |
| 19.12.2016 Monday | 111 | Information Security | Computer Networks | Power Quality |
| 21.12.2016 Wednesday | IV | Principles & Applications of Embedded Systems | Mobile Cellular Communication | High Voltage Engineering |
| 23.12.2016 Friday | ν | Mobile Computing | Embedded Systems | |
| 26.12.2016 Monday | VI | Adhoc And Sensor Networks | Industrial Administration and Financial Management | |

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