

**ES I/2017**

**UNIVERSITY OF KERALA  
CAREER RELATED FIRST DEGREE PROGRAMME UNDER CBCSS GROUP 2(b)**

**PRACTICAL TIME TABLE FOR THE FIFTH SEMESTER B.Sc.  
ELECTRONICS (CBCSS) REGULAR/SUPPLEMENTARY  
DEGREE EXAMINATION, NOVEMBER 2016**

<b>Date</b>	<b>Subject</b>	<b>College Code</b>
01.03.2017 to 07.03.2017	EX 1544: Seminar	135,801,802,803,804,805,806,807,823, 824,950
01.03.2017 to 07.03.2017	EX 1545: Project Design	135,801,802,803,804,805,806,807,823, 824,950
01.03.2017 to 07.03.2017	EX 1546: Communication Lab & Software Lab II	135,801,802,803,804,805,806,807,823, 824,950

**CONTROLLER OF EXAMINATIONS**

University Buildings  
Thiruvananthapuram  
Dated : 22.02.2015

MT/C3