UNIVERSITY OF KERALA
Timetable for Fifth Semester B.Tech Degree Examination, December 2016 / January 2017
2013 Scheme
(REGULAR CANDIDATES ONLY)

Monday	19/12/2016	1.30 pm to 4:30pm	Engineering Mathematics IV(CMPUHBS) Engineering Mathematics IV (E) Engineering Mathematics IV – Complex Analysis & Linear Algebra (TARF) Introduction to Stochastic Models (N)
Wednesday	21/12/2016	1.30 pm to 4.30pm	Environmental Engineering I (C) Electrical Technology(U) Operations Research (N) Heat Transfer Operations I (H) Mass Transfer Operations(B) Gas Dynamics(S) Synchronous Machines (E) Engineering Management for Electronics Engineers(AT) Engineering Mathematics V(FR)(Advanced Mathematics and Queuing Models)
Friday	23/12/2016	2.00 pm to 5:00 pm	Structural Analysis II (C) Industrial Electronics(MP) Work Study and Ergonomics (N) Automotive Transmission (U) Particle Technology(H) Enzyme Engineering and Technology (B) Machine Design(S) Switchgear and Protection (E) Microprocessors and Microcontrollers (AT)
Wednesday	04/01/2017	1.30 pm to 4:30 pm	Geotechnical Engineering I (C) Mechanics of Materials (M) Production Process I(P) Mechanics of Machinery (U) Object Oriented Programming and Numerical Methods (N) Chemical Reaction Engineering I (H) Principles of Heat Transfer in Bioprocesses (B) Aircraft Structures II (S) Electronic Measurements and Instrumentation(T) Electrical Machines & Drives (A) Systems Programming (FR)
Friday	06/01/2017	2.00 pm to 5.00 pm	Transportation Engineering I (C) Machine Tools (MN) Machine Tools II (P) Auto Electrical /Electronics (U) Chemical Engineering Thermodynamics(H) Thermodynamics of Bioprocesses(B) Experimental Stress Analysis (S) Applied Electromagnetic Theory (T) Power Electronics (A) Microprocessors and Interfacing (R) Theory of Computation (F)

Monday	09/01/2017	1.30 pm to 4:30 pm	Water Resources Engineering(C) Elective I (MPU) Elective I (N) Elective I (H) Genetic Engineering (B) Elective I (S) Elective I (E) Elective I (AT) or (T) or (A) Object Oriented Design and JAVA Programming (R) Software Architecture and Design Patterns (F)
Wednesday	11/01/2017	1.30 pm to 4:30 pm	Theory of Machines (MP) Operating Systems(FR) Control Systems (E)
Thursday	12/01/2017	1.30 pm to 4:30 pm	Electronic Instrumentation (E)

University Buildings,
Thiruvananthapuram

CONTROLLER OF EXAMINATIONS