

  
**KANNUR UNIVERSITY**  
**(Examination Branch)**

**I Sem. M.Tech R/S Exmn April 2014**

**ELECTRONICS & COMMUNICATION ENGINEERING**

**M. Tech in VLSI Design & Signal Processing (VDSP)**

**Practical Examination-schedule**

**Centre: LBS College of Engineering Kasaragod**

**Subject : VDSP 107 (P): Digital Signal Processing Laboratory**

<b>Date</b>	<b>Slot</b>	<b>Register Numbers</b>	<b>Number of Students</b>
04/11/2014 (Tuesday)	9:30 – 12:30	B3MTEC 3301 to B3MTEC3306	6
	11:00 – 2:00	B3MTEC 3307 to B3MTEC3312	6
	1:00 – 4:00	B3MTEC 3313 to B3MTEC3318	6