

## **MAHATMA GANDHI UNIVERSITY**

No. EAI/2/1/06

P.D. Hills,  
13.12.2016

### **NOTIFICATION**

It is hereby notified that the II Semester (new scheme 2015 admission regular & 2014 admission supplementary) M.Sc. Applied Electronics degree examinations are scheduled to be conducted from 06.01.2017 as per the details given below.

#### **TIME TABLE**

**(Time: 9.30 am to 12.30 pm on all days)**

Day & Date	Subjects
Friday 06.01.2017	Microwave Device and Systems
Monday 09.01.2017	Communication Systems
Wednesday 11.01.2017	Microprocessor (8086) & Microcontroller Systems
Friday 13.01.2017	Digital Signal Processing

The last date for the receipt of applications in the University office without fine is 19.12.2016, with a fine of `50/- is 20.12.2016 and with a superfine of `500/- is 23.12.2016. The Regular candidates shall also remit a sum of `100/- towards fee for Provisional degree Certificate.

Sd/-

DR. M.THOMAS JOHN  
Controller of Examinations

To

1. The Director/Regional Directors, UCsAS
2. PRO for a Press Release/Enquiry
3. PS to VC/PVC
4. JRI/III/DR II/VIII/AR XI/XII/XVIII/XXI/Pool Officer(Exams)/  
System Administrator
5. PA to CE/C.E's sn/CT&D sn/ENI/IV/V/CETEX/Front Office
6. EBV/VIII/ EH XIV /Ac C/Ac.AV/EKSns/Infor.Centres/FRIENDS
7. Exam Store/Exam Tappal/Gen.Cash Sns.
8. SF/FC

Approved for Issue,

**SECTION OFFICER**