

**ES I/2016**

**UNIVERSITY OF KERALA**  
**CAREER RELATED FIRST DEGREE PROGRAMME UNDER CBCSS-**  
**GROUP 2 (b)**  
**TIME TABLE FOR THIRD SEMESTER B.SC ELECTRONICS DEGREE**  
**EXAMINATION, JANUARY 2017**  
**2015 Admission (Regular)**

**TIME: 1.30 PM TO 4.30 PM, Friday 2.00 PM to 5.00 PM**

<b>Day &amp; Date</b>	<b>Course Code</b>	<b>Course</b>	<b>Course Title</b>
<b>09.01.2017 Monday</b>	<b>EX 1341</b>	<b>Core course</b>	<b>Electronic Circuits</b>
<b>11.01.2017 Wednesday</b>	<b>EX 1342</b>	<b>Core course</b>	<b>Communication Engineering</b>
<b>13.01.2017 Friday</b>	<b>EX 1343</b>	<b>Core course</b>	<b>Microprocessor and Interfacing</b>
<b>17.01.2017 Tuesday</b>	<b>MM 1331.8</b>	<b>Complementary Course</b>	<b>Mathematics III (2013 Admn onwards)</b>
<b>19.01.2017 Thursday</b>	<b>EX 1332</b>	<b>Complementary Course</b>	<b>Computer Organization</b>

**Controller of Examinations**

University Buildings,  
Thiruvananthapuram  
Dated: 30.12.2016

