



**K L UNIVERSITY**  
**Examination Section**  
**I/II M.Tech II Semester Supplementary Examinations November 2016 (Y14 Batch)**  
**TIME TABLE**

**Timings: 09.30 AM to 12.30 PM**

M.Tech	24.11.2016	25.11.2016	26.11.2016	28.11.2016	29.11.2016	30.11.2016
<b>Biotechnology</b>	Plant and Animal Biotechnology 12BT505	Immunotechnology 12BT506	Bio Reactor Modeling and Simulation 12BT507	Down Stream Processing 12BT508	Molecular Modeling and Drug Designing 12BT E533	Enzyme Technology 12BT E534
<b>Structural Engineering</b>	Finite Element Analysis 11CE601	Bridge Engineering 11CE602	Earth Quake Resistant Design of Structures 11CE603	Theory of Plates and Shells 11CE604	Industrial Structures 11CE631	Green Buildings 11CE643
<b>Construction Technology &amp; Management</b>	Mechanized Construction & Machinery 14CT601	Project Formulation Appraisal 14CT602	Construction Laws and Regulations 14CT603	Quality Management and Safety Management Systems in Construction 14CT604	Deep Excavations and Ground Water Control Methods 14CT632	Construction and Fire Safety 14CT643
<b>Computer Science and Engineering</b>	Operating System Design 14CS506	Computer Networks and Security 14CS507	Object Oriented Software Engineering 14CS508	Enterprise Programming 14CS509	Mobile Computing 14CS539	Big Data Analytics 14CS545
<b>Computer Networks</b>	Performance Analysis of Computer Networks 14CN506	Wireless Networks & Mobile Computing 14CN507	Network Security 14CN508	Wireless Network Security 14CN509	Cyber Forensics 14CN542	Intrusion Detection & Prevention System 14CN543
<b>Cloud Computing</b>	Parallel Algorithms 14CC506	Cloud Security 14CC507	Mobile Cloud 14CC508	Data Center Virtualization 14CC509	Cloud Application Architectures 14CC540	Object Oriented Software Engineering 14CC543
<b>Communication and Radar Systems</b>	Advanced Digital Signal Processing 13EC521	Radar Signal Processing 13EC522	Wireless Cellular Communication 13EC504	CMOS RF Circuit Design 13EC566	Adaptive Signal Processing 13EC530	Wireless Sensor Networks 13EC583
<b>VLSI</b>	Advanced Analog IC Design 13EC570	Low Power VLSI Circuits 13EC555	VLSI System Design 13EC556	Testing of VLSI Circuits 13EC571	System on Chip Design 13EC562	Nano Sensors and Its Applications 13EC593
<b>Signal Processing</b>	Advanced Digital Signal Processing 13EC521	Radar Signal Processing 13EC522	Detection and Estimation of Signals 13EC537	DSP Processors and Architectures 13EC528	Adaptive Signal Processing 13EC530	Optical Signal Processing 13EC534
<b>Power Systems</b>	Real Time Control of Power System 14EE513	Micro Controllers and Embedded Systems 14EE506	EHVAC & HVDC 14EE514	Power System Digital Protection 14EE515	Energy Conservation Audit 14EE545	AI Techniques in Power Systems 14EE546
<b>Power Electronics and Drives</b>	Advanced Power Converters 14EE505	Micro Controllers and Embedded Systems 14EE506	Modeling and Simulation of Power Electronic Systems 14EE507	Industrial Application of Electronics 14EE508	Power Quality 14EE537	Advance PWM Techniques 14EE548
<b>Embedded Systems</b>	Advanced Embedded Processor Architectures 11EM601	Digital Signal Processors and Architectures 13EM602	Hardware Software Co-Design 11EM603	Linux System Concepts 13EM604	Embedded Networking 13EM E32	Advanced Computer Networks 11EME42
<b>Wireless Communication and</b>	MEMS and NEMS 13EM515	Communications Protocols and Standards 13EM516	Wireless Sensor Networks 13EM517	Design and Analysis of Algorithms 13EM518	CDMA and OFDM for Wireless communications 13EM542	Reliability Engineering 13EM545
<b>Thermal Engineering</b>	Incompressible and Compressible flows 13TE601	Computational Fluid Dynamics 13TE602	Refrigeration and Cryogenics 13TE603	Measurements in Thermal Engineering 13TE604	Gas Turbine Engineering 13TE632	Renewable Energy Technology 13TE642
<b>Mechatronics</b>	Robotics: Advanced Concepts and Analysis 13MT601	Control of Mechatronic Systems 13MT602	Mechatronics Product Design 13MT603	Precision Engineering 13MT604	Computational Fluid Dynamics 13MT531	Industrial Automation 13MT631

**Note: Practical Examinations for Supplementary candidates will commence after completion of Theory Examinations. Detailed Time Table will be given by the Examination Branch.**

**Controller of Examinations**

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