

VFSTR UNIVERSITY:: VADLAMUDI
Time Table for B.Tech. I Year I & II Semester (R09) Supplementary Examinations – November 2016
(for 2009 Admitted Batch only)

Release Date: 29.09.2016

Examination Time: 10.00 AM to 01.00 PM

Date	BRANCH	2009 Batch
11.11.2016 (Friday)	BT / CSE / IT	Data Structures (CS201)
	ECE	Circuit Theory (EC198)
	Mech	Basic Electronics & Electrical Engineering (EE102)
14.11.2016 (Monday)	Common to All Branches	C Programming (CS102)
16.11.2016 (Wednesday)	Common to All Branches	Engineering Mathematics - I (HS143)
18.11.2016 (Friday)	Common to All Branches	Engineering Mathematics - II (HS145)
21.11.2016 (Monday)	Civil / Mech / MCT	Engineering Mechanics (ME198)
	ECE	Elements of Electronics and Communication Engineering (EC199)
23.11.2016 (Wednesday)	Common to All Branches	Engineering Physics (HS147)
25.11.2016 (Friday)	CSE/ ECE / EEE / MCT / IT	Electronic Devices and Circuits (EC203)
28.11.2016 (Monday)	Common to All Branches	Engineering Drawing (ME109)


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VU/CE/CIR/16/678 (TS) Date: 28.09.2016